



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76956

Shinya WATANABE, et al.

Appln. No.: 10/648,276

Group Art Unit: 2813

Confirmation No.: 4435

Examiner: Jennifer M. DOALN

Filed: August 27, 2003

For: SEMICONDUCTOR CHIP HAVING AN ARRAYED WAVEGUIDE GRATING AND
METHOD OF MANUFACTURING THE SEMICONDUCTOR CHIP AND MODULE
CONTAINING THE SEMICONDUCTOR CHIP

PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136

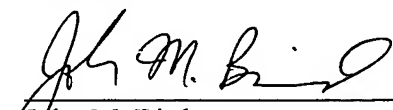
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of two months, extending the time for responding to the Office Action of January 5, 2005 to June 6, 2005 (June 5, 2005 being a Sunday).

A check for the statutory fee of \$450.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this sheet is enclosed.

Respectfully submitted,



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WASHINGTON OFFICE

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CUSTOMER NUMBER

Date: June 6, 2005